

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10431	((programmable with gate with array) FPGA) and (schedul\$4 same time)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:34
L2	2917	((programmable with gate with array) FPGA) and (schedul\$4 same time) and (time with delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:34
L3	2191	((((programmable with gate with array) FPGA) and (schedul\$4 with time)) and (time with delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:34
L4	129	((((programmable with gate with array) FPGA) same (schedul\$4 with time)) and (time with delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:34
L5	96	((((programmable with gate with array) FPGA) same (schedul\$4 with time)) and (time with delay) and event	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:35
L6	43	((((programmable with gate with array) FPGA) same (schedul\$4 with time)) and (time with delay) and event and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:35
L7	50	((((programmable with gate with array) FPGA) same (schedul\$4 with time)) and (time with delay) and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:35

L8	13849	703/13 703/15 703/16 703/19 703/23 703/28 714/33 716/5 716/6 716/16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:39
L9	238	I1 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:39
L10	15	I4 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 15:39
L15	4	jean.in. and petrolli.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:06
L16	1420	((programmable with gate with array) FPGA) and ((schemul\$4 same time) same (delay conflict))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:07
L17	47	I16 and I8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:07
L18	50	((((programmable with gate with array) FPGA PLD) same (schemul\$4 with time)) and (time with delay) and simulat \$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:08
L19	51	((((programmable with (logic gate) with array) FPGA PLD) same (schemul\$4 with time)) and (time with delay) and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:08

L20	53	((((programmable with (logic gate) with array) FPGA PLD) same (schedul\$4 with time)) and (time same delay) and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:08
L21	62	((((programmable with (logic gate) with array) FPGA PLD) same (schedul\$4 with time)) and (time same delay \$3) and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:08
L22	64	((((programmable with (logic gate) with array) FPGA PLD) same (schedul\$4 with time)) and ((time timing) same delay\$3) and simulat\$5 and emulat\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/09/22 19:08

EAST Search History (Interference)

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